

Date: March 15, 2004

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Patents, P.O. Box 1450, Alexandria, VA 22313-1450



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): Daniel C. Worledge

Docket No.:

YOR920030410US1

Serial No.:

10/699,284

Filed:

October 31, 2003

Group: -

2818

Examiner:

Unassigned

Title:

Techniques for Coupling in Semiconductor Devices

TRANSMITTAL OF FORMAL DRAWINGS

Signature:

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Applicants submit herewith three (3) sheets of formal drawings and an Associate Power of Attorney in the above-referenced application.

Respectfully submitted,

Date: March 15, 2004

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